Supervisors and Reset ICs Quick Reference Guide

TEXAS INSTRUMENTS

Overview

Supply voltage supervisors, also known as reset ICs or voltage monitors, are used to monitor the system power supply. These devices supervise voltage rails in order to ensure proper power on, detect fault conditions, and perform a system hand-shake with embedded processors. For more information on the various supervisor specifications, see the **design factor glossary**. To learn more and view our complete portfolio, visit: www.ti.com/supervisors.

Fixed delay time

		Monitored Voltages (V)											_	Reset				_	
Device	# of Channels Monitored	1.2	1.8	2.5	3	3.3	5	12	Other	Package (s)	V _{DD} Range (V)	Ι _{DD} (typ) (μΑ)	Time Delay (ms)	Threshold Accuracy [§] (max) (%)	Manual Reset/ Enable	Active-Low Reset/ Output	Active-High Reset/ Output	Reset Output Topology	Automotive
TPS3831	1	V	V	-	V	V	V	-	~	SON-4	0.9 to 6.5	0.15	200	±1.6	~	~	_	PP	_
<u>TPS3839</u>	1	r	~	-	v	v	V	-	~	SON-4, SOT-23-3	0.9 to 6.5	0.15	200	±1.6	—	~	_	PP	—
TLV803	1	—	—	V	V	V	V	-	—	S0T23-3	1.1 to 6	9	200	±2.2	—	~	_	OD	—
TLV809	1	—	—	V	V	V	V	-	—	S0T23-3	2 to 6	9	200	±2.2	_	v	_	PP	—
TLV810	1	—	—	V	V	V	V	-	—	S0T23-3	1.1 to 6	9	200	±2.2	_	—	~	PP	—
LM809	1	—	—	-	V	V	V	-	—	S0T23-3	1 to 5.5	15	240	±2.8	_	~	_	PP	—
LM810	1	—	—	-	-	-	V	-	—	S0T23-3	1 to 5.5	15	240	±2.8	—	—	~	PP	_
TPS3847	1	—	—	-	-	-	_	~	—	S0T-23-5	4.5 to 18	0.38	20	±2.5	~	~	_	PP	—

Programmable delay time

	# of	Monitored Voltages (V)						V)						Reset Threshold	Manual	Active-Low	Active-High	Reset	
Device	Channels Monitored	Adj.	1.2	1.8	2.5	3	3.3	5	Other	Package (s)	V _{DD} Range (V)	ι _{DD} (typ) (μΑ)	Time Delay	Accuracy [§] (max) (%)	Reset/ Enable	Reset/ Output	Reset/ Output	Output Topology	Automotive
TPS3808	1	•	~	~	~	~	~	~	~	SOT-23-6 SON-6	1.7 to 6.5	2.4	1.25ms to 10s	±2	V	~	—	OD	⊻
<u>TPS3890</u>	1	\mathbf{v}^{\dagger}	V	V	~	V	~	\mathbf{v}^{\dagger}	V	SON-6	1.5 to 5.5	2.1	40µs to 30s	±1	V	~	—	OD	<u>~</u>
TPS3895/7	1	\mathbf{v}^{\dagger}	✓†	\checkmark^{\dagger}	\mathbf{v}^{\dagger}	V	\mathbf{v}^{\dagger}	\mathbf{v}^{\dagger}	\checkmark^{\dagger}	SON-6	1.7 to 6.5	6	40µs to 10s	±1	V	—	V	PP/OD	—
TPS3896/8	1	\mathbf{v}^{\dagger}	r	V	V	V	\mathbf{v}^{\dagger}	\mathbf{r}^{\dagger}	V	SON-6	1.7 to 6.5	6	40µs to 10s	±1	~	~	—	PP/OD	_
<u>LP3470</u>	1	-	-	-	—	V	~	V	~	S0T-23-5	0.5 to 5.5	16	200µs to 60s	±1.5	_	~	—	OD	—
LM8365	1	—	—	—	_	V	—	V	—	S0T-23-5	1 to 6	0.62	—	±2.5 (typ)	_	V	—	OD	—
TPS3860x0	4	\mathbf{v}^{\dagger}	V	\mathbf{v}^{\dagger}	\mathbf{v}^{\dagger}	\mathbf{v}^{\dagger}	\checkmark^{\dagger}	\mathbf{v}^{\dagger}	\checkmark^{\dagger}	QFN-20	1.8 to 6.5	9	1.4ms to 10s	±1	V	~	V	PP + OD	⊻

No delay time (voltage detector)

	# of		Monitored Voltages (V)															Propa-	Reset Threshold	Manual	Active-Low	Activo High	Reset	
Device	Channels Monitored	Adj.	1.2	1.5	1.8	2.5	3	3.3	5	12	15	19	24	Other	Package (s)	V _{DD} Range (V)	ι _{DD} (typ) (μΑ)	gation Delay (typ) (µs)	Accuracy [§] (max) (%)	Reset/ Enable	Reset/ Output	Active-High Reset/ Output	Output Topology	Automotive
TPS3803	1	✓†	V	V	1	\mathbf{v}^{\dagger}	V	\mathbf{v}^{\dagger}	v †	∕ †∗	✓†*	$\mathbf{V}^{\dagger \star}$	V ^{†*}	\checkmark^{\dagger}	SC70-5	1.3 to 6	3		±1.5	_	~	—	OD	<u>~</u>
<u>TPS3710</u>	1	✓†	✓†	V	V	V	•†	•†	•†	v †	✓†	V ^{†*}	✓⁺∗	r	ThinSOT23-6, SON-6	1.8 to 18	7	18	±1	_	~	_	OD	_
TPS3711	1	\mathbf{v}^{\dagger}	\mathbf{v}^{\dagger}	\mathbf{v}^{\dagger}	•	\mathbf{v}^{\dagger}	V	\mathbf{v}^{\dagger}	v †	\mathbf{v}^{\dagger}	✓†	\mathbf{v}^{\dagger}	✓†	\mathbf{v}^{\dagger}	ThinS0T23-6	1.8 to 36	7	9.9	±0.75	_	~	—	OD	—
<u>LMP7300</u>	1	✓†	-	-	-	V	•†	✓†	•†	v †	✓†	V	✓†	r	SOIC-8, VSSOP-8	2.7 to 12	13	5	—	_	Confi	gurable	OD	_
<u>TLV3011</u>	1	•	-	V	•	V	•†	•†	•	✓†*	✓†*	✓†*	✓⁺∗	r	SOT23-6, SC70-6	1.8 to 5.5	2.8	8	+1.75/-2	_	Confi	gurable	OD	_
<u>TLV3012</u>	1	V	-	V	•	V	•†	∕ †	•†	✔†*	✓⁺∗	V ^{†*}	✓⁺∗	r	SOT23-6, SC70-6	1.8 to 5.5	2.8	8	+1.75/-2	_	Confi	gurable	PP	<u>~</u>
LMS33460	1	-	-	-	-	-	-	V	-	-	-	-	-	-	SC70-5	0.8 to 7	130		±5 (typ)	-	~	_	OD	—
TPS3779	2	\checkmark^{\dagger}	\mathbf{v}^{\dagger}	V	•†	V	V	\mathbf{v}^{\dagger}	v †	✓†*	✓†*	V ^{†*}	V ^{†*}	\mathbf{v}^{\dagger}	SON-6	1.5 to 6.5	1.8	5.5	±1	—	v	—	PP	<u>~</u>
TPS3780	2	\mathbf{v}^{\dagger}	\mathbf{v}^{\dagger}	\mathbf{v}^{\dagger}	V	\mathbf{v}^{\dagger}	V	\mathbf{v}^{\dagger}	V	$oldsymbol{\prime}^{\dagger\star}$	$\checkmark^{\dagger \star}$	$\mathbf{V}^{\dagger \star}$	${oldsymbol{ u}}^{\dagger\star}$	\mathbf{v}^{\dagger}	SON-6	1.5 to 6.5	1.8	5.5	±1	-	~	_	OD	<u>~</u>

[†]Not a fixed voltage threshold

[§]Does not factor in external resistor divider tolerance *Device can monitor but cannot operate off of rail

Window comparator + internal reference

			Monitored Voltages (V)															Propa-	Reset					
Device	# of Channels Monitored	Adj.	1.2	1.5	1.8	2.5	3	3.3	5	12	15	19	24	Other	Package (s)	V _{DD} Range (V)	I _{DD} (typ) (μΑ)	gation Delay (typ) (µs)	Threshold Accuracy [§] (max) (%)	Manual Reset/ Enable	Active-Low Reset/ Output	Active-High Reset/ Output	Reset Output Topology	Automotive
<u>TPS3700</u>	1	•	✓ [†]	•†	•†	•	•†	V	•†	✓†	∕ †	~ ^{†*}	✓†*	r	ThinSOT23-6, SON-6	1.8 to 18	5.5	18	±1	_	V	~	OD	⊻
TPS3701	1	V	V	V	V	\mathbf{v}^{\dagger}	/ †	\mathbf{v}^{\dagger}	✓†	\mathbf{v}^{\dagger}	\mathbf{v}^{\dagger}	\mathbf{v}^{\dagger}	\mathbf{v}^{\dagger}	\mathbf{v}^{\dagger}	ThinSOT23-6	1.8 to 36	7	9.9	±0.75	—	~	~	OD	_
TPS3702	1	✓†	V	-	V	V	V	V	V	\mathbf{v}^{\dagger}	\checkmark^{\dagger}	1 *	✓ [†] *	V	ThinSOT23-6	1.8 to 18	7	19	±0.9	—	~	~	OD	<u>~</u>
LMV7231	6	V	V	V	V	V	v †	\mathbf{v}^{\dagger}	/ †	✓⁺∗	V [†] *	1 *	V ^{†*}	\mathbf{v}^{\dagger}	QFN-24	2.2 to 5	7.7/ch	2.6	+2.34/-2.47	—	~	~	OD	—

Supervisor + watchdog timer

		Monitored Voltages (V)														Reset					
Device	# of Channels Monitored	Adj.	1.2	1.5	1.8	2.5	3	3.3	5	Other	Package (s)	V _{DD} Range (V)	I _{DD} (typ) (μΑ)	Time Delay (typ) (ms)	Watchdog Timer WDI (sec)	Threshold Accuracy [§] (max) (%)	Manual Reset/ Enable	Active-Low Reset/ Output	Active-High Reset/ Output	Reset Output Topology	Automotive
TPS3850	1	\mathbf{v}^{\dagger}	V	-	V	-	V	V	V	—	DFN-10	1.6 to 6.5	10	Prog.	Adj. window	±0.8	—	~	_	OD	<u>~</u>
TPS3851	1	—	-	-	V	V	V	V	V	—	DFN-8	1.6 to 6.5	10	200	Adj.	±0.8	~	~	—	OD	⊻
TPS3852	1	—	-	-	-	-	-	V	—	—	DFN-8	1.6 to 6.5	10	200	Adj. window	±0.8	~	~	—	OD	⊻
TPS3123/4	1	—	V	V	V	-	-	-	—	—	SOT-23-5	0.75 to 3.3	14	180	1.4	±3.6	v /—	~	—/ v	PP	—
TPS3128	1	—	V	V	V	-	-	-	-	—	S0T-23-5	0.75 to 3.3	14	180	1.4	±3.5	~	~	—	OD	—
TPS3813	1	—	-	-	-	V	V	V	V	—	SOT-23-6	2 to 6	9	25	Adj. window	±2.2	—	~	—	OD	⊻
TPS3820/8	1	—	-	-	-	-	-	V	V	—	S0T-23-5	1.1 to 5.5	15	25/200	0.2/1.6	±2.4	~	~	—	PP	<u>v</u> / <u>v</u>
TPS3823/4	1	—	-	-	-	V	V	V	V	—	S0T-23-5	1.1 to 5.5	15	200	1.6	±2.4	v /—	~	—/ v	PP	<u>v</u> / <u>v</u>
TPS3110	2	\mathbf{v}^{\dagger}	V	V	-	-	-	V	—	~	SOT-23-6	0.9 to 3.3	1.2	130	1.1	±0.75 (typ)	~	~	—	PP	—
TPS3305	2	—	-	-	V	V	-	V	V	—	SO-8, MSOP-8	2.7 to 6	15	200	1.6	±3	~	~	~	PP	—
TPS3705	2	\mathbf{v}^{\dagger}	-	-	-	-	V	V	V	—	SO-8, MSOP-8	2 to 6	30	200	1.6	±4	~	~	—	PP	—
TPS3306	3	${\bf v}^{\dagger}$	—	V	V	V	-	V	V	V	SO-8, MSOP-8	2.7 to 6	15	100	0.8	±3.2	—	~	_	OD	⊻
TPS3860x0	4	\mathbf{v}^{\dagger}	\mathbf{v}^{\dagger}	V	V	V	V	\mathbf{v}^{\dagger}	✓†	\mathbf{v}^{\dagger}	QFN-20	1.8 to 6.5	9	Prog.	0.6	±1	V	~	 ✓ 	OD	⊻

Push-button reset IC

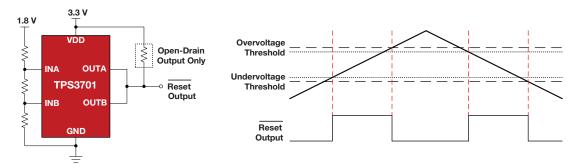
Device	# of Channels Monitored	Input Delay (s)	Reset Pulse Width (ms)	Package (s)	V _{DD} Range (V)	I _{DD} (typ) (μΑ)	Active-Low Reset/ Output	Active-High Reset/ Output	Reset Output Topology	Automotive
TPS3420	2	7.5/12.5	Input dependent	SON-6	1.6 to 6.5	0.35	~	_	OD	—
TPS3421	2	0/7.5	80	SON-6	1.6 to 6.5	0.25	~	—	OD	—
TPS3422	1	0/7.5	400	SON-6	1.6 to 6.5	0.25	~	_	OD	_

[†]Not a fixed voltage threshold [§]Does not factor in external resistor divider tolerance *Device can monitor but cannot operate off of rail

Why supervisors and reset ICs?

Window Comparator with Integrated Reference

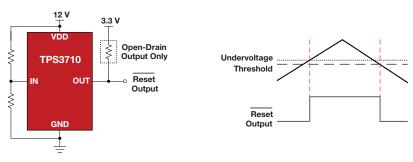
Detect overvoltage fault events in addition to undervoltage fault events to alert a system if a voltage rail deviated from its permissible tolerance.



Why supervisors and reset ICs? (continued)

Wide input voltage

For voltage rails greater than 5 V, TI's family of wide-input voltage detectors can both operate from *and* monitor 12-V, 15-V, 24-V rails and more.



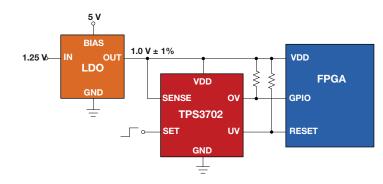
Low power + small size

TI's family of small-size, low-power supervisors help address PCB size constraints, improve system efficiency and extend battery life.



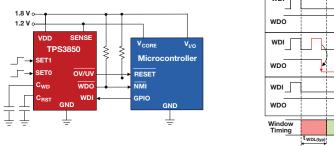
Accurate voltage detection

For supply rails with tight tolerance specifications, such as those found in processors and FPGAs, TI's high accuracy voltage supervisors detect fault events and notify the system to ensure proper operation.



Integrated supervisor + watchdog timer

Coupling a voltage supervisor with a watchdog timer allows supply voltage monitoring of the processor as well as internal fault detection. The watchdog timer adds critical redundancy to systems where a processor may freeze or hang.

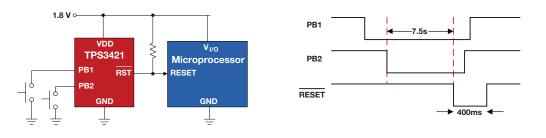


WDI			
WDO		Correct Operation	
WDI		Early Fault	
WDO	Ľ		
WDI		Late Fault	
WDO			
Window Timing		Valid Window	
	t _{WDL(typ)}	t wDU(typ)	,

Why supervisors and reset ICs? (continued)

Push-button reset IC

Being able to reset a system by holding down a button or two is critical to applications where it is not easy access to toggle the power supply or remove the battery. In such cases, a push-button reset IC can monitor these user presses and provides a reset after a given time period has been met.



Design factors

- **Reset delay time** Time delay between the fault event and the reset output asserting. Delays range between 10µs and 30s, and can either be fixed or programmed via an external capacitor.
- Reset output polarity The reset output can assert high (Active-High) or low (Active-Low) in the event of a fault.
- Reset output topology The reset output comes in two flavors: open-drain (OD) and push-pull (PP). An open-drain (or open-collector) output requires the output be tied to an external voltage via a pull-up resistor. A push-pull (or CMOS) output both sinks and sources current without needing to be tied to an external voltage.
- Package TI's Reset ICs are available in leaded (ex. SOT23-3) and unleaded packages (ex. X2SON). The smallest
 voltage supervisors in TI's portfolio are 1 mm².
- Watchdog Timer (WDT) Monitors a periodic pulse from a processor and provides a reset signal to the processor when that pulse has not been received within a designated time period. This increases redundancy in systems that can't afford an undetected processor hang or freeze.
- Supply Current (I_{DD}) Voltage supervisor current draw. Minimizing the supply current is critical in extending battery life and ensuring more efficient system operation.
- Manual Reset (MR) / Enable (EN) Allows manual intervention to assert the reset output.
- Voltage threshold accuracy Determines how close the actual threshold voltage is to the target threshold voltage.
- **AEC-Q100 Qualification** Many of TI's voltage supervisors are qualified for use in automotive applications. This is indicated by a "-Q1" suffix. Other devices can be qualified upon request.

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